

## II. AMENDMENTS TO THE SPECIFICATION:

Please make the following amendments to the specification:

On page 10, para. [0031], please amend as follows:

"Generate" or "render," as used herein, means that unique design feature 60 is built in such a way that designer 2 (FIG. 1), using system 10, can meaningfully use the data, e.g., system 10 may display, generate data for performance evaluation, or perform any other task that a system 10 may now or later provide. A "design grid" 68 (FIG. 2B) (~~FIG. 1~~) can be any well-known grid implemented by an EC designer 24 used for laying out a floorplan, or map, of an electronic circuit. Control point CP is positioned on design grid 68 according to control point positioning data in a template recall of the DIF 26, as will be described in greater detail below. That is, each template's unique design feature 60 is not positioned relative to a previous feature, but rather relative to the origin of the design grid. In particular, as shown in FIGS. 2A, 2B, each template has an offset instruction 72 (FIG. 4) that allow rendering of a respective unique design feature 60A, 60B as though it includes a bounding orthogonal polygonal border 74 with a respective control point CP located at a corner of the polygonal border.